



PATENT APPLICATION  
042390.P10047

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: ) August 12, 2003  
)  
Jian Li et al. )  
)  
Serial No.: 09/897,174 ) Group Art Unit: 2826  
)  
Filed: June 29, 2001 ) Examiner: T. Dickey

For: **LOW-VOLTAGE AND INTERFACE DAMAGE-FREE POLYMER  
MEMORY DEVICE**

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING  
DEPOSITED WITH THE UNITED STATES POSTAL SERVICE IN  
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ENVELOPE ADDRESSED TO:

HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK  
OFFICE,

WASHINGTON, D.C. 20231, ON: 8.13.03  
Date of Deposit

INTEL CORPORATION  
Name of Assignee

[Signature] 8.13.03  
SIGNATURE DATE

**RESPONSE**

HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK  
OFFICE,  
Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed June 4, 2003, please reconsider  
the above-identified patent application in view of the remarks below.